PCN Number:			2017	20171017000			PCN Date:	Nov. 7, 2017		7	
Tit	tle:	Datasheet fo	r PGA	460-Q	1, F	PGA460					
Customer Contact: PCN M			<u> Manager</u>				Dept:		Quality Services		
Ch	ange	Туре:									
	Asse	embly Site				Design				Wafer	Bump Site
Assembly Process				□ Data Sheet				Wafer Bump Material			
Assembly Materials				Part number change					Wafer	Bump Process	
Mechanical Specification				Test Site					Wafer	Fab Site	
Packing/Shipping/Labeling			ng	Test Process				Wafer Fab Materials			
									Wafer	Fab Process	
	Notification Details										
	-										

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below.

The following change history provides further details.



PGA460-Q1

SLASEC8A - FEBRUARY 2017 - REVISED AUGUST 2017

Cr	anges from Original (February 2017) to Revision A	Page
•	Added zero padding information to the CONFIGURATION/STATUS Command section	31
•	Changed UART interface parameter text from: 1 stop bit to: 2 stop bit	33
•	Changed interfield wait time text from: optional to: required for 1 stop bit	33
•	Added sentence: The sync field (0x55) is not included as part of the checksum calculation.	36
•	Updated content and added tablenotes to Table 3	37
•	Added sentence: The diagnostic field is included in the slave generated checksum calculation.	40
•	Added subsection Direct Data Burst Through USART Synchronous Mode	45
•	Added Equation 8	50
•	Added sentence: This includes all threshold timing and level values.	52
•	Updated UART and USART Communication Examples content	55
•	Updated content in Table 101	105
•	Added content to Application Curves	107
•	Added content to <i>Direct-Driven (Transformer-Less) Method</i> and changed Figure 141 such that a GND node is present at XDCR _{Negative} and C _{INN}	108
•	Changed text from: TDK EPCOS B78416A2232A03 Transformer, muRata MA40H1S-R transducer to: Fairchild FDC6506P p-channel MOSFET, muRata MA40H1S-R transducer	109



Changes from Original (April 2017) to Revisi	on A	Page		
Added zero padding information to the CON	IFIGURATION/STATUS Command	section31		
Changed UART interface parameter text from: 1 stop bit to: 2 stop bit				
Changed interfield wait time text from: optional to: required for 1 stop bit				
Added sentence: The sync field (0x55) is no	ot included as part of the checksum	calculation		
Updated content and added tablenotes to Ta	able 3	37		
Added sentence: The diagnostic field is incli	uded in the slave generated checks	um calculation 40		
Added subsection Direct Data Burst Through	h USART Synchronous Mode	45		
Added Equation 8		50		
Added sentence: This includes all threshold timing and level values.				
Updated UART and USART Communication Examples content				
Updated content in Table 101				
Added content to Application Curves				
Added content to <i>Direct-Driven (Transforme</i> present at XDCR _{Negative} and C _{INN}				
Changed text from: TDK EPCOS B78416A2 FDC6506P p-channel MOSFET, muRata M				
he datasheet number will be chang	ing.			
Device Family	Change From:	Change To:		
PGA460-Q1	SLASEC8	SLASEC8A		
PGA460	SLASEJ4	SLASEJ4A		
hese changes may be reviewed at t	the datasheet links provide	d.		
ttp://www.ti.com/product/PGA460-	<u>·Q1</u>			
ttp://www.ti.com/product/PGA460				
Reason for Change:				

Reason for Change:

To reflect the device pin descriptions and the interconnections in the figure accurately.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

PGA4601PWQ1	PGA460TPWQ1	PGA460TPWRQ1	PGA460TPW	PGA460TPWR
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For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com